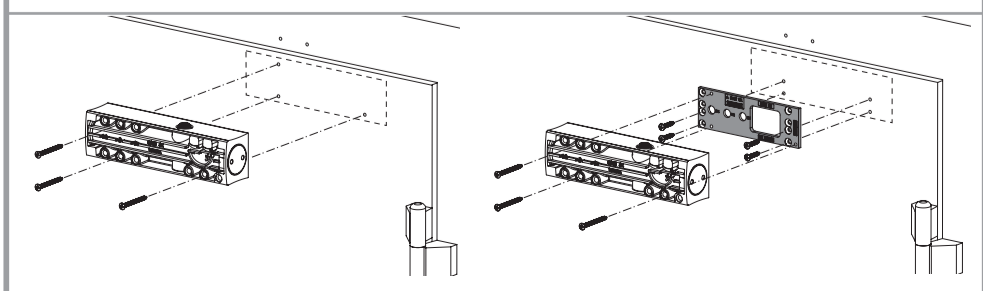
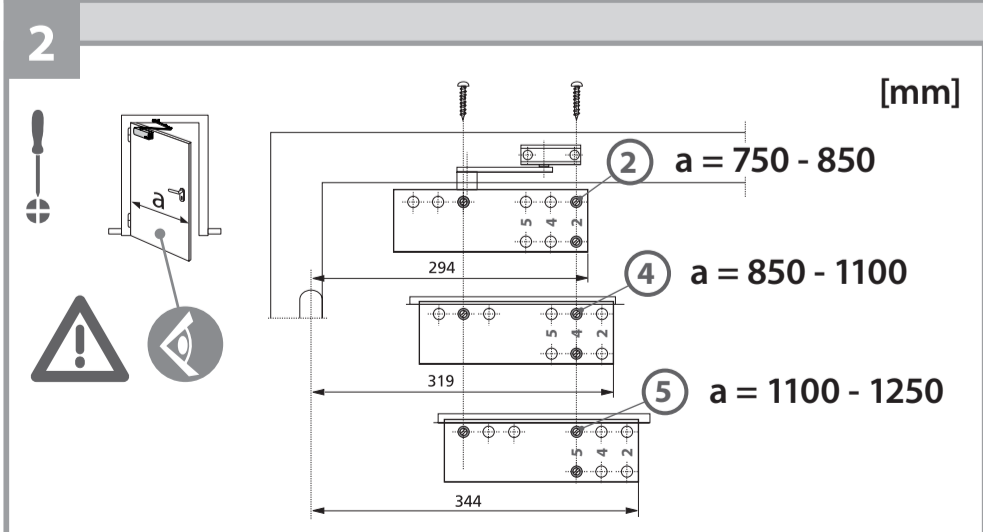
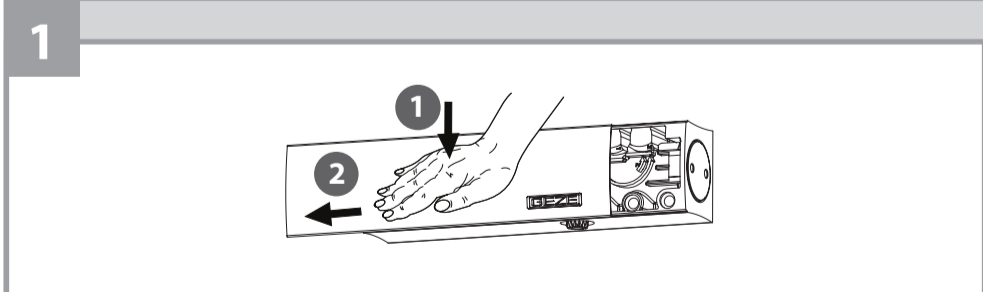
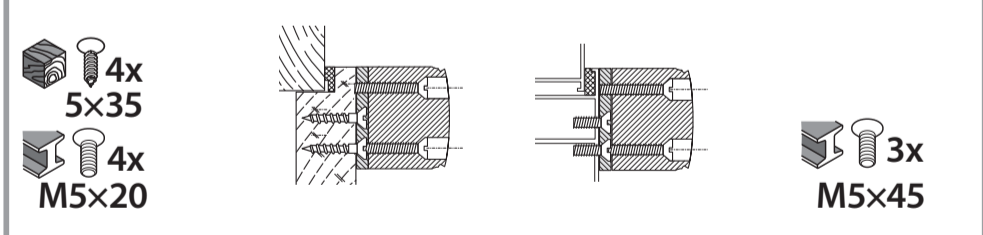
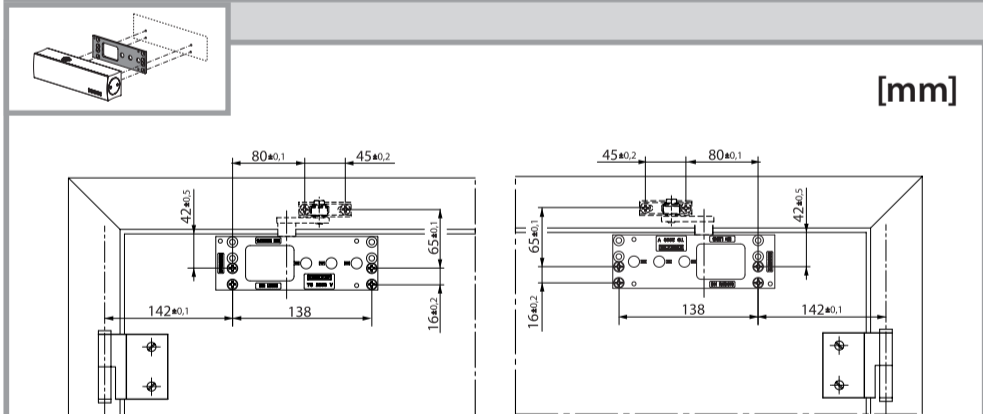
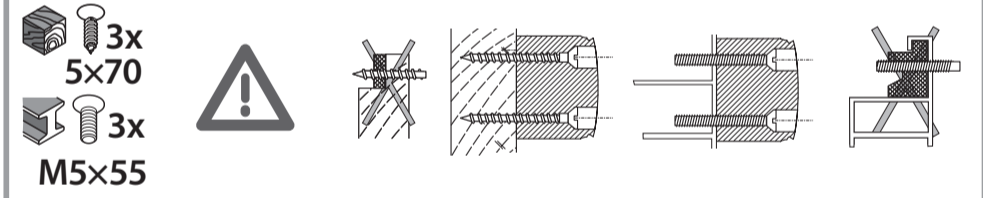
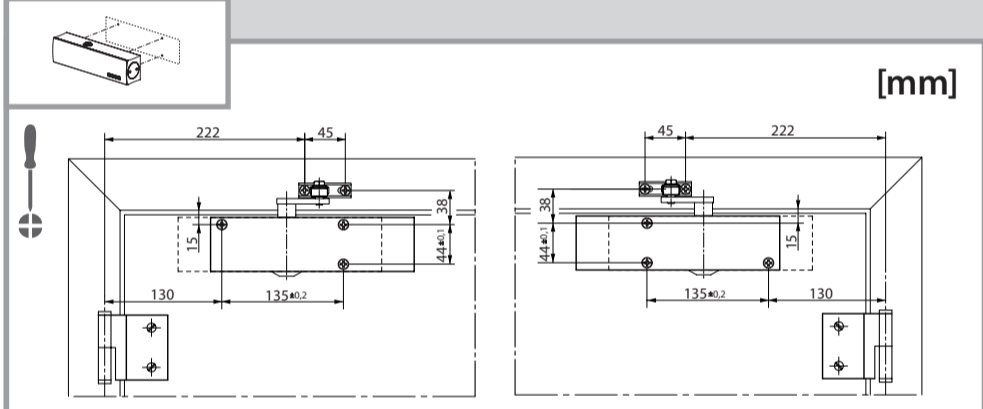
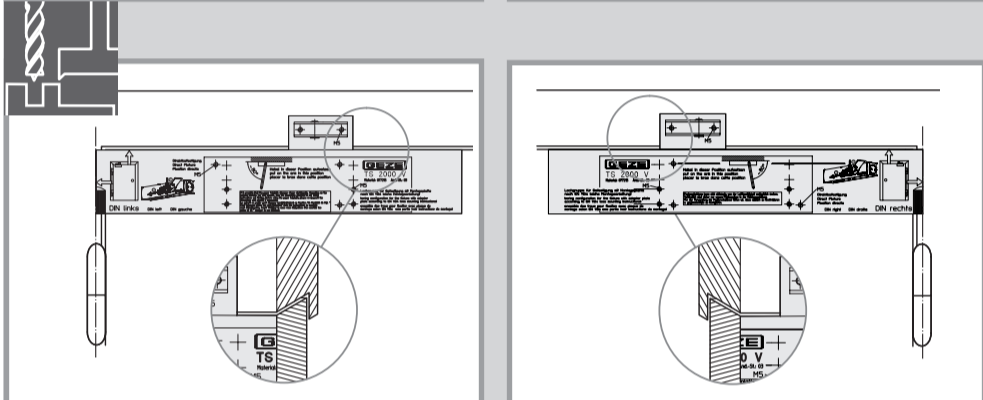
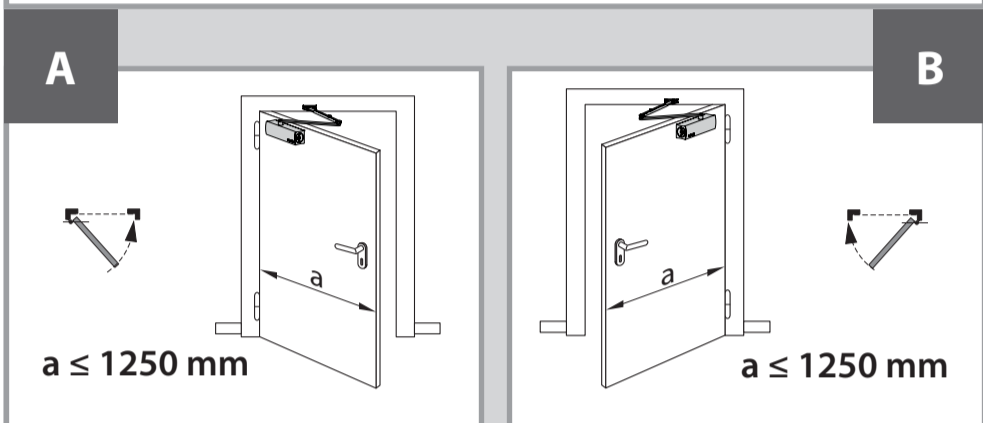


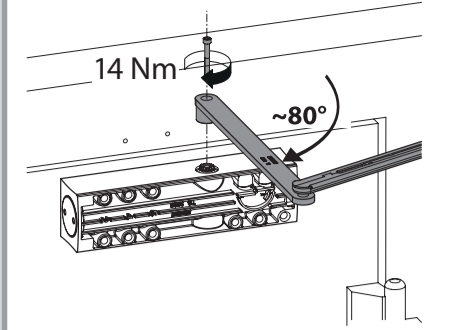
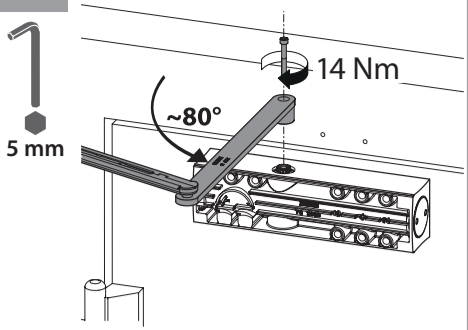
TS 2000 V  
TS 2000 V BC



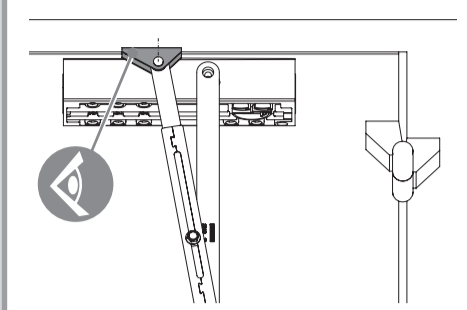
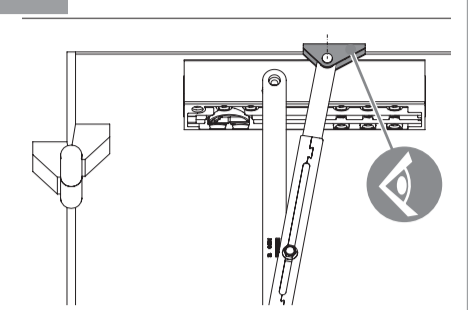
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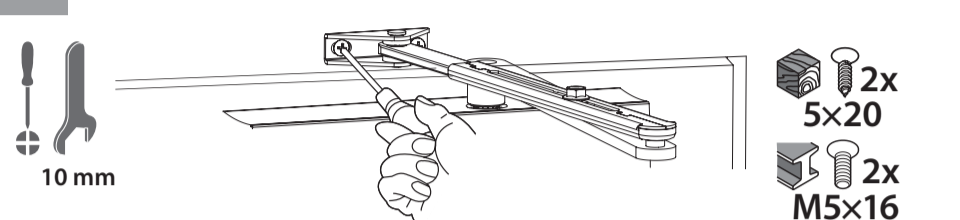
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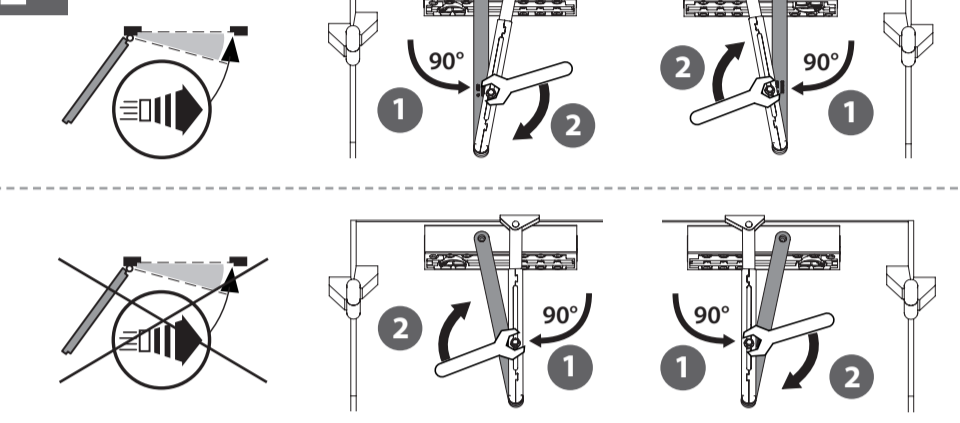
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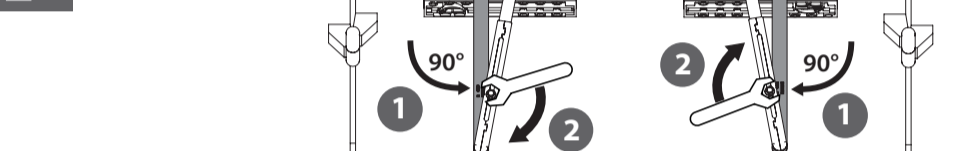
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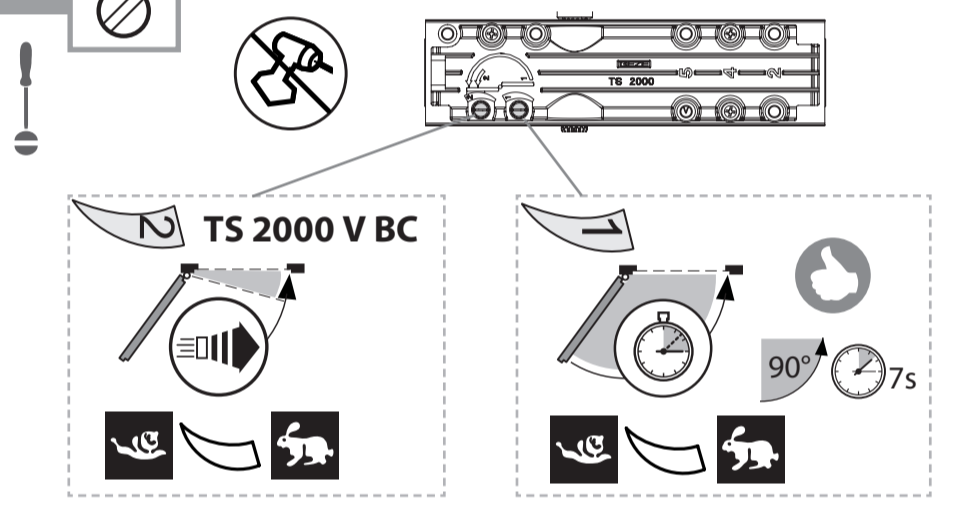
**i+** TS 2000 V



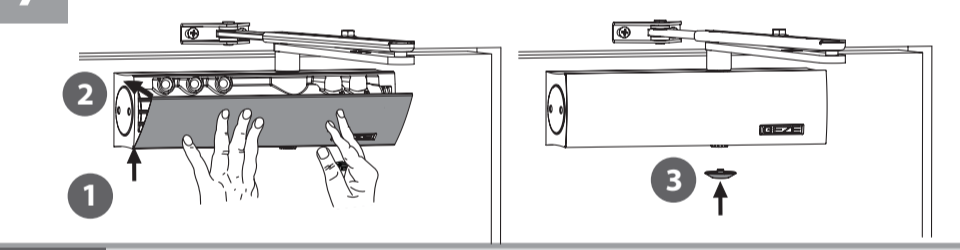
**i+** TS 2000 V BC



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7



**i**

